

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: N. YAMAMOTO, et al.

Serial No: To be assigned

Filed: January 21, 2004

Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
METHOD FOR MANUFACTURING THE SAME

Group: n/a

Examiner: n/a

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 21, 2004

Sir:

The following amendments and remarks are submitted in the above-identified application at the time of filing and as set forth on the following pages:

Amendments to the Specification and Abstract

Remarks are included following the amendments